

Ingress

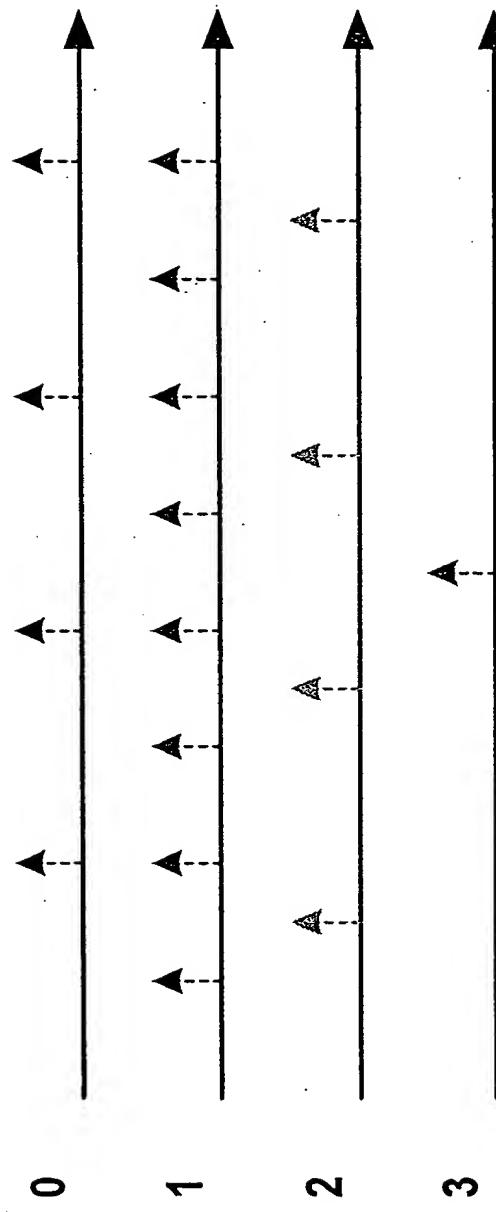


Fig. 1a

**Multicast
Schedule**

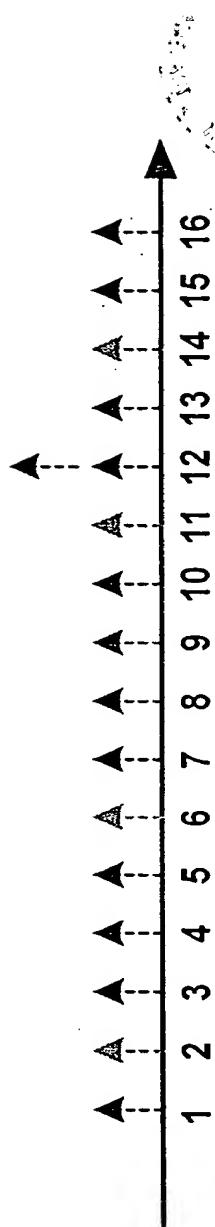


Fig. 1b

Fig. 2a

Multicast Cell Fanout Table

Fig. 2c

Compile Control Frame

Fig. 2d

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Fig. 2e

Multicast Egress Credit

Fig. 2b

Fig. 2

27	0	35	5
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A vertical stack of 10 numbered squares, numbered 1 through 10 from top to bottom. Each square contains a small grid of numbers. A dashed arrow points to the right from the top of the stack.